AR-B1832

Pentium M inside,EPIC form factor ,On Board VGA,LVDS with DDR SO-DIMM, built in LAN,CF Type-II

> Edition: 1.0 Book Number: AR-B1832-06.12.29

@Copyright 2005

All Rights Reserved.

Manual first edition Nov 22, 2005

The information in this document is subject to change without prior notice in order to improve reliability, design and function and does not represent a commitment on the part of the manufacturer.

In no event will the manufacturer be liable for direct, indirect, special, incidental, or consequential damages arising out of the use or inability to use the product or documentation, even if advised of the possibility of such damages.

This document contains proprietary information protected by copyright. All rights are reserved. No part of this manual may be reproduced by any mechanical, electronic, or other means in any form without prior written permission of the manufacturer.

Trademarks

AR-B1832 is registered trademarks X-Fire Acrosser, IBM PC is a registered trademark of International Business Machines Corporation. Pentium is a registered trademark of Intel Technologies, Inc. Award is registered trademarks of Award Software International, Inc. Other product names mentioned herein are used for identification purposes only and may be trademarks and/or registered trademarks of their respective companies.

Contents

Cont	ent	S3	5
Intro	duc	tion5	
1	1.1	Specifications:	5
1	1.2	What You Have7	7
Insta	llati	on 8	3
2	2.1	AR-B1832's Layout	3
2	2.2	Power Button Setting	
2	2.3	CMOS Reset 12	2
2	2.4	Jumper description 12	2
Conr	nect	ion 14	ŀ
Э	3.1	Ultra ATA33/66/100 IDE Disk Drive Connector(IDE1) 14	ł
Э	3.2	Serial Ports(COM1~4)15	5
Э	3.3	Keyboard / Mouse Connector(MS_KB1) 16	3
Э	3.4	USB Port Connector(USB1~4) 16	3
Э	3.5	Fan Connector (FAN1) 17	7
Э	3.6	LAN RJ45 Connector (LAN1) 17	7
Э	3.7	Compact Flash Storage Card Socket(CF1)17	7
3	8.8	VGA Connector(VGA1)19)

AR-B1832

	3.9	AUDIO Connector(AUDIO1) 19	9
	3.10	DDR SODIMM Socket (J1) 19	9
	3.11	8-BIT GPIO Connector(GPIO1) 20	C
	3.12	Parallel port(PRN1)20	C
	3.13	LVDS Connector(LVDS1)	1
	3.14	Speak Connector(SPK1)2	1
Awa	ard B	BIOS Setup22	2
	4.1	Introduction	2
	4.2	Starting Setup 22	2
	4.3	Using Setup23	3
	4.4	Main Menu24	4
	4.5	Advanced BIOS Features	7
	4.6	PnP/PCI Configuration Setup 29	Э
	4.7	Peripheral	C
	4.8	PC Health	2
	4.9	Boot	3
	4.10	Exit Selecting	4
Арр	endi	x A. Watchdog Timer 36	3
Арр	endi	x B: Digital I/O37	7
Арр	endi	x C: I/O Address Map 39)

AR-B1832

1 Introduction

1.1 Specifications:

CPU : Supports Intel Pentium M, Celeron M CPU.

Chipset : GMCH 852GM and ICH4 82801DB

RAM memory : DDR SDRAM SO-DIMM Socket support to 1GB/266MHz.

Display Controller: Intel 855GME Supports non-interlaced CRT monitors Supports LVDS Encoders.

Ultra DMA 100 IDE Interface : One Enhance IDE channel.

CompactFlash[™] interface : Supports CompactFlash[™] Type II socket for Compact Flash Disk or IBM Micro Drive.

Series ports : Four high-speed 16C550 compatible UARTs ports.COM4 can also support RS-422/485.

Parallel Port: IEEE-1284 compliant. Supports SPP/EPP/ECP mode.

USB port : Support Six USB 2.0 compatible ports.

Audio Connector: supports Line-in, Line-out, MIC-in.

Digital IO: Supports eight digital-in, and eight digital-out TTL-level I/O ports.

IrDA: Supports Serial Infrared(SIR) or Amplitude Shift Keyed IR(ASKIR)interface.

PS/2 Mouse/Keyboard Connector

Watchdog timer : Time setting form 1 to 255 second / minute System Reset generate when CPU did not periodically trigger the timer.

AR-B1832

Intel LAN Controller: One ports IEEE 802.3u Auto-Negotiation support for Intel 82551ER 10/100BASE-TX. Connected to your LAN through RJ45 connector.

Power Consumption : 12V / 3.5A

Operating Temperature : -10° ~ 60° C (CPU needs Cooler)

Dimension: 115mm(W) X 165mm(L)

AR-B1832

1.2 What You Have

In addition to this *User's Manual*, the AR-B1832 package includes the following items:

AR-B1832 board

User Manual

Drive CD

AR-B1832

2 Installation

This chapter describes how to install the AR-B1832. At first, the layout of AR-B1832 is shown, and the unpacking information that you should be careful is described. The jumpers and switches setting for the AR-B1832's configuration

2.1 AR-B1832's Layout



AR-B1832



Bottom Placement

AR-B1832



AR-B1832

1

1 || |2

1 9 2

> 1 • • 3

2.2 Power Button Setting

CN9 : Power Connector

Pin	DESCRIPTION	
1	+12V	2
2	GND	



Pin	DESCRIPTION
1	+5VSB
2	Power ON

• D3 : Power LED / HDD LED

LED	DESCRIPTION	
GREEN	POWER LED	
YELLOW	HDD LED	

CN6 : Reset Button Connector

Pin	DESCRIPTION
Open	Normal
Short	Reset System

• CN8 : Power ON Pin Header

Pin	DESCRIPTION
1	GND
2	PS_ON
3	5VSB

2.3 CMOS Reset

• JP1 : CMOS pin header

JP1	DESCRIPTION
1-2	Norr al Operation
2-3	Clear CMOS



.

2.4 Jumper description

• JP2 : Select CF Master or Slave mode

JP2	Description
Short	Master
Open	Slave

CN3: Keyboard Lock

CN3	Description	
Open	Unloc <	2
Short	Lock	2

• JP3 : COM1/2 Select RI is 12V or signal

PIN	DESCRIPTION	PIN	DESCRIPTION
1	NRIA	2	NRIA_12V
3	+12V	4	NRIA_12V
5	NRIB	6	NRIB_12V
7	+12V	8	NRIB_12V

• JP4 : Select COM4 is RS232 or RS422/485

JP4	Description	
1-2	RS2: 2	*
2-3	RS422/485	3

CN4 : Case Open

CN4	Description	1
Open	Norm al	
Short	Power off	2

AR-B1832

• JP5 : If Short SERIRQ Connect to PC104+ Pin B1

JP5	Description	1
1	J2 Pin B1	
2	SERIRQ	1
		2

• IR1 : Infrared Pin Header

PIN	Description	
1	+5V	6
2	NC	0
3	RX	0
4	GND	0
5	TX	1 🗖
6	VCC2	

• JP6 : Select LCD Voltage

JP6	Description
1-2	+3.3V
2-3	+5V

1 • • 3

•

CN7 : Inverter Power Connector

PIN NO.	DESCRIPTION
1	+12V
2	+12V
3	GND
4	BKLTEN
5	GND
6	BKLTCTL

AR-B1832

3

Connection

This chapter describes how to connect peripherals, switches and indicators to the AR-B1832 board.

3.1 Ultra ATA33/66/100 IDE Disk Drive Connector(IDE1)

You can attach two IDE(Integrated Device Electronics) hard disk drives to the AR-B1832 IDE controller.

PIN NO.	DESCRIPTION	PIN NO.	DESCRIPTION	1		12
1	RESET#	2	GROUND	-	0 0	[-
3	DATA 7	4	DATA 8			
5	DATA 6	6	DATA 9			
7	DATA 5	8	DATA 10			
9	DATA 4	10	DATA 11			
11	DATA 3	12	DATA 12			
13	DATA 2	14	DATA 13			
15	DATA 1	16	DATA 14			
17	DATA 0	18	DATA 15			
19	GROUND	20	N/C			
21	N/C	22	GROUND			
23	IOW#	24	GROUND			
25	IOR#	26	GROUND			
27	N/C	28	BALE - DEFAULT			
29	N/C	30	GROUND - DEFAULT			
31	INTERRUPT	32	IOCS16#-DEFAULT			
33	SA1	34	N/C			
35	SA0	36	SA2			
37	HDC CS0#	38	HDC CS1#			
39	HDD ACTIVE#	40	GROUND			
41	+5V LOGIC	42	+5V MOTOR			
43	GROUND	44	TYPE	43	Ľ	4 4

IDE 1 : Secondary IDE Connector (44 Pins)

AR-B1832

3.2 Serial Ports(COM1~4)

The AR-B1832 offers two high speeds NS16C550 compatible UARTs with Read/Receive 16 byte FIFO serial ports.

• COM1 : RS-232 Serial port

PIN	DESCRIPTION	PIN	DESCRIPTION
1	-DCD	2	RXD
3	TXD	4	-DTR
5	GND	6	-DSR
7	-RTS	8	-CTS
9	-RI	10	NC



• COM2/3 : RS-232 Serial port (Pin Header)

PIN	DESCRIPTION	PIN	DESCRIPTION
1	-DCD	2	-DSR
3	RXD	4	-RTS
5	TXD	6	-CTS
7	-DTR	8	-RI
9	GND	10	NC

2	2	ł	ł	ł	ľ	10
1	l	ł		ł	ł	 9

 COM4 : RS-232 with RS-422/485 Serial 	port((Pin Header)
--	-------	--------------

PIN	DESCRIPTION	PIN	DESCRIPTION
1	-DCD	2	-DSR
3	RXD	4	-RTS
5	TXD	6	-CTS
7	-DTR	8	-RI
9	GND	10	GND
11	TX+	12	TX-
13	RX+	14	RX-

²₁¹⁴₁₃

AR-B1832

3.3 Keyboard / Mouse Connector(MS_KB1)

A PS/2 type connector(MS_KB1)is for easy connection to a keyboard and PS/2 mouse. The board comes with a Y split PS/2 cable for keyboard and mouse connection.

• MS_KB1 : Keyboard Mouse PS2 Port

PIN	DESCRIPTION	PIN	DESCRIPTION
1	KB_DAT	2	MS_DAT
3	GND	4	+5V
5	KB_CLK	6	MS_CLK
7	GND	8	GND



3.4 USB Port Connector(USB1~4)

The AR-B1832 provides six USB port, four pin header, two connectors . • USB1 : USB Connector

PIN	DESCRIPTION	PIN	DESCRIPTIO N
1	VCC	5	VCC
2	USB0-	6	USB1-
3	USB0+	7	USB1+
4	GND	8	GND

• USB2/3 : USB Connector(Pin header)

PIN	DESCRIPTION	PIN	DESCRIPTION
1	VCC	2	VCC
3	USB3/4_0-	4	USB3/4_1-
5	USB3/4_0+	6	USB3/4_1+
7	GND	8	GND
9	USB_GND	10	USB_GND

9	1
Ш	П
	П
10	2

AR-B1832

3.5 Fan Connector (FAN1)

The AR-B1832 provides one connectors for CPU cooling fan they can be controlled by Super I/O Chip.

• FAN1: Fan Connector for CPU

PIN NO.	DESCRIPTION	1
1	GND	
2	+12V	
3	PWM Signal	3

3.6 LAN RJ45 Connector (LAN1)

AR-B1832 is equipped with built-in 10/100Mbps Ethernet Controller. You can connect it to your LAN through RJ45 LAN connector. The pin assignments are as following:

• LAN1 : LAN RJ45 Connector

PIN NO.	DESCRIPTION	PIN NO.	DESCRIPTION
1	TX+	5.	N/C
2	TX-	6.	RX-
3.	RX+	7.	N/C
4.	N/C	8.	N/C



3.7 Compact Flash Storage Card Socket(CF1)

The AR-B1832 configures Compact Flash Storage Card in IDE Mode. This type II Socket is compatible with IBM Micro Drive.

•CF1 : Compact Flash Storage Card Socket pin assignment



AR-B1832

PIN NO.	DESCRIPTION	PIN NO.	DESCRIPTION
1	GROUND	26	CARD DETECT1
2	D3	27	D11
3	D4	28	D12
4	D5	29	D13
5	D6	30	D14
6	D7	31	D15
7	CS1#	32	CS3#
8	N/C	33	N/C
9	GROUND	34	IOR#
10	N/C	35	IOW#
11	N/C	36	PULL HIGH
12	N/C	37	IRQ15
13	VCC	38	VCC
14	N/C	39	MASTER/SLAVE
15	N/C	40	N/C
16	N/C	41	RESET#
17	N/C	42	IORDY
18	A2	43	N/C
19	A1	44	PULL HIGH
20	A0	45	ACTIVE#
21	D0	46	PDIAG#
22	D1	47	D8
23	D2	48	D9
24	N/C	49	D10
25	CARD DETECT2	50	GROUND

Note: If IDE2 & CFD1 both in used, CFD1 must be as "Master" & IDE2 is as "Slave".

AR-B1832

3.8 VGA Connector(VGA1)

The AR-B1832 has a built-in 15-pin VGA connector accepting the CRT monitor

VGA1 : 15-pin D-Sub Connector

PIN	DESCRIPTION	PIN	DESCRIPTION
1	L_RED	2	L_GREEN
3	L_BLUE	4	MON2PU
5	GND	6	GND
7	GND	8	GND
9	+5V	10	GND
11	MONOPU	12	5VDDCDA
13	HSYNC	14	VSYNC
15	5VDDCCL		



R 9

3.9 AUDIO Connector(AUDIO1)

• AUDIO1 : Audio Pin Header

PIN	DESCRIPTION	PIN	DESCRIPTION	10 🖷
1	LINE_OUT_R	2	LINE_OUT_L	10
3	GND	4	GND	
5	LINE_IN_R	6	LINE_IN_L	E
7	MIC_IN	8	GND	28
9	GND	10	GND	

3.10 DDR SODIMM Socket (J1)

There are two 200-pin DDR SDRAM DIMM slots to accept 2.5V non_buffered DDR SDRAM. The max Memory size is 2GB.

• J1 : DDR SODIMM Socket



AR-B1832

3.11 8-BIT GPIO Connector(GPIO1)

• GPIO1: 8 BIT GPIO Connector

PIN	DESCRIPTION	PIN	DESCRIPTION	
1	GP10	2	GP11	_
3	GP12	4	GP13	2
5	GP14	6	GP15	
7	GP16	8	GP17	1
9	GND	10	VCC	_

3.12 Parallel port(PRN1)

This port is usually connected to a printer. The AR-B1832 includes an on-board parallel port.

 PRN1: Parallel Port Connect 	or
---	----

PIN	DESCRIPTION	PIN	DESCRIPTION
1	STB-	14	AFD-
2	PD0	15	ERR-
3	PD1	16	INIT-
4	PD2	17	SLIN-
5	PD3	18	GND
6	PD4	19	GND
7	PD5	20	GND
8	PD6	21	GND
9	PD7	22	GND
10	ACK-	23	GND
11	BUSY	24	GND
12	PE	25	GND
13	SLCT	26	Х



AR-B1832

30

3.13 LVDS Connector(LVDS1)

• LVDS1 : LVDS Interface Connector

PIN	DESCRIPTION	PIN	DESCRIPTION	
2	GND	1	LVDS_PWR	
4	LVDS_CLKBP	3	LVDS_CLKBM	
6	LVDS_YBM2	5	GND	
8	GND	7	LVDS_YBP2	
10	LVDS_YBP1	9	LVDS_YBM1	
12	LVDS_YBM3	11	LVDS_YBP3	
14	LVDS_YBM0	13	LVDS_YBP0	
16	LVDS_CLKAP	15	GND	
18	GND	17	LVDS_CLKAM	
20	LVDS_YAM2	19	LVDS_YAP2	
22	LVDS_YAP1	21	DDCPCLK_X	
24	DDCPDATA_X	23	LVDS_YAM1	
26	LVDS_YAM0	25	LVDS_YAP0	
28	LVDS_YAM3	27	LVDS_YAP3	
30	LVDS_PWR	29	LVDS_PWR	

3.14 Speak Connector(SPK1)

• SPK1 : Speak out Connector(through Amplifier)

PIN NO.	DESCRIPTION
1	SPKR
2	GND
3	SPKL
4	GND



AR-B1832

4

Award BIOS Setup

4.1 Introduction

This chapter discusses the Setup program built into the BIOS. The Setup program allows users to configure the system. This configuration is then stored in battery-backed CMOS RAM so that it retains the Setup information while the power is off.

4.2 Starting Setup

The BIOS is immediately active when you turn on the computer. While the BIOS is in control, the Setup program can be activated in one of two ways:

- 1. By pressing immediately after switching the system on, or
- By pressing the key when the following message appears briefly at the bottom of the screen during the POST (Power On Self-Test).

Press DEL to enter SETUP.

If the message disappears before you respond and you still wish to enter Setup, restart the system to try again by turning it OFF then ON or pressing the "RESET" button on the system case. You may also restart by simultaneously pressing <Ctrl>, <Alt>, and <Delete> keys. If you do not press the keys at the correct time and the system does not boot, an error message will be displayed and you will again be asked to...

PRESS F1 TO CONTINUE, DEL TO ENTER SETUP

AR-B1832

4.3 Using Setup

In general, you can use the arrow keys to highlight items, press <Enter> to select, use the PageUp and PageDown keys to change entries, press <F1> for help and press <Esc> to quit. The following table provides more details about how to navigate in the Setup program using the keyboard.

Key	Function	
Up Arrow	Move to the previous item	
Down	Move to the next item	
Arrow		
Left Arrow	Move to the item on the left (menu bar)	
Right Arrow	Move to the item on the right (menu bar)	
Esc	Main Menu: Quit without saving changes	
	Submenus: Exit Current page to the next higher level	
	menu	
Move Enter	Move to the item you desired	
PgUp key	Increase the numeric value or make changes	
PgDn key	Decrease the numeric value or make changes	
+ key	Increase the numeric value or make changes	
- key	Decrease the numeric value or make changes	
Esc key	Exit Menu Quit and not save changes into CMOS	
	Status Page Setup Menu and Option Page Setup	
	Menu Exit current page and return to Main Menu	
F1 key	General help on Setup navigation keys	
F5 key	Load previous values from CMOS	
F6 key	Load the fail-safe defaults from BIOS default table	
F7 key	Load the optimized defaults	
F10 key	Save all the CMOS changes and exit	

AR-B1832

4.4 Main Menu

The items in Standard CMOS Setup Menu are divided into 10 categories. Each category includes no, one or more than one setup items. Use the arrow keys to highlight the item and then use the <PgUp> or <PgDn> keys to select the value you want in each item.

Phoenix - Main Advanced Peripherals	AwardBIOS CMOS Setup Ut PnP/PCI PC Health Bo	ility ot Exit
Date (mm:dd:yy) Time (bh:mm:ss)	Wed, Nov 24 1999 10 : 26 : 10	Item Help
 IDE Primary Master IDE Primary Slave IDE Secondary Master IDE Secondary Slave 	[None]	Menu Level Change the day, month, year and century
Drive A Drive B	[1.44M, 3.5 in.] [None]	
Video Halt On	[EGA/VGA] [All , But Keyboard]	
Base Memory Extended Memory Total Memory	640K 1K 1024K	
1↓→+:Move Enter:Select +/- F5:Previous Values F6	/PU/PD:Value F10:Save :Fail-Safe Defaults F	ESC:Exit F1:General Help 7:Optimized Defaults

Figure 1: The Main Menu

Main Menu Selections

l em	Options	Description
Date	MM DD YYYY	Set the system date.
Time	HH : MM : SS	Set the system time
IDE	Options are in its sub	Press <enter> to enter</enter>
Primary Master	menu	the sub menu of detailed
	(described in Table 3)	options
IDE	Options are in its sub	Press <enter> to enter</enter>
Primary Slave	menu	the sub menu of detailed
	(described in Table 3)	options
IDE	Options are in its sub	Press <enter> to enter</enter>
Secondary Master	menu	the sub menu of detailed
	(described in Table 3)	options
IDE	Options are in its sub	Press <enter> to enter</enter>

AR-B1832

Secondary Slave	menu	the sub menu of detailed
	(described in Table 3)	options
Drive A & Drive B	None	Select what kind of floppy
	360K, 5.25 in	type you install
	1.2M, 5.25 in	
	1.720K, 3.5 in	
	1.44K, 3.5 in	
	2.88K, 3.5 in	
Video	EGA / VGA	Select what type of
	CGA 40	Display you use
	CGA 80	
	Mono	
Halt On	All Errors	Select the situation in
	No Errors	which you want the BIOS
	All, but Keyboard	to stop the POST
	All, but Diskette	process and notify you
	All, but Disk/Key	
Base Memory	N/A	Displays the amount of
		conventional memory
		detected during boot up
Extended Memory	N/A	Displays the amount of
		extended memory
		detected during boot up
Total Memory	N/A	Displays the total
		memory available in the
		system

Table 1 Main Menu Selections

IDE Adapters

The IDE adapters control the hard disk drive. Use a separate sub menu to configure each hard disk drive. Figure 2 shows the IDE primary master sub menu.

IDE HDD Auto-Detection[Press Enter]

IDE Primary Master[Auto] Access Mode [Auto] Capacity0MB

AR-B1832

Cylinder0 Head0 Precomp0 Landing Zone0 Sector0

Figure 2 IDE Primary Master sub menu

Use the legend keys to navigate through this menu and exit to the main menu. Use Table 2 to configure the hard disk.

Item	Options	De scription
IDE HDD Auto-detection	Press Enter	Press Enter to auto-detect the HDD on this channel. If detection is successful, it fills the remaining fields on this menu.
IDE Primary Master	None Auto Manual	Selecting 'manual' lets you set the remaining fields on this screen. Selects the type of fixed disk. "User Type" will let you select the number of cylinders, heads, etc. Note: PRECOMP=65535 means NONE !
Capacity	Auto Display your disk drive size	Disk drive capacity (Approximated). Note that this size is usually slightly greater than the size of a formatted disk given by a disk checking program.
Access Mode	CHS LBA Large Auto	Choose the access mode for this hard disk

Table 2 Hard disk selections

AR-B1832

4.5 Advanced BIOS Features

This section allows you to configure your system for basic operation.

Phoenix - AwardBIOS CMOS Setup Utility		
Main Advanced Peripherals	<u>s PnP/PCI PC Health Bo</u>	ot Exit
APIC Mode Quick Power On Solf Test	[Enabled]	Item Help
USB Keyboard Support PS/2 Mouse Function	[Disabled] [Disabled] [Enabled]	Menu Level ►
Init Display First Intel VGA Share Memory Boot Display × Panel Type	[Onboard/AGP] [32MB] [CRT] 640×480	
ACPI Function × ACPI Suspend Type PWRON After PWR-Fail Power-Supply Type	[Disabled] S1(POS) [Last State] [AT]	
↑↓++:Move Enter:Select +/- F5:Previous Values F6	-/PU/PD:Value F10:Save 5:Fail-Safe Defaults F	ESC:Exit F1:General Help 7:Optimized Defaults

Figure 3 Advanced menu

APIC Mode

This item allows use Advanced Programmable Interrupt Controller feature.

The Choice: Enabled, Disabled.

Quick Power On Self Test

This category speeds up Power On Self Test (POST) after you power up the computer. If it is set to Enable, BIOS will shorten or skip some check items during POST.

Enabled	Enable quick POST
Disabled	Normal POST

Full Screen LOGO Show

This item allows you to enable or disable show full screen LOGO. The Choice: Enabled, Disabled.

USB Keyboard Support

This item allows you to enable or disable USB keyboard support. The Choice: Enabled, Disabled.

AR-B1832

PS/2 Mouse Function

Disabled-prevents any installed PS/2 mouse from functioning but frees up IRQ12.Enabled-allows the operating system to determine whether to enable or disable the mouse. Choice: Enabled, Disabled.

Init Display First

This item allows you to choose which Display to be first detected. The Choice: PCI Slot, On Board / AGP.

Intel VGA Share Memory

This item allows you to Choose the Frame Buffer size for Display. The Choice: 1MB, 4MB, 8MB, 16MB, 32MB.

Boot Display

This item allows you to choose display interface. The Choice: Vbios default, CRT, EFP, TV, CRT + EFP, CRT + TV.

Panel Type

This item allows you to choose display panel type and resolution. The Choice: 640x480,800x600,1024x768.

ACPI Function

This item allows you to enable or disable Advanced Configuration and Power Management (ACPI) function. The Choice: Enabled, Disabled.

ACPI Suspend Type

This item allows you to Choose Suspend Type for ACPI function. The Choice: S1(Pos), S3(STR), S1 & S3.

Power Supply Type

This item allows you to choose the Type of Power Supply in use. The Choice: AT, ATX.

PWRON After Power-Fail

This item allows you to choose the Option of Power Status after Power Fail by ATX Power Supply. The Choice: Former-STS, On, Off.

AR-B1832

4.6 PnP/PCI Configuration Setup

Phoenix - AwardBIOS CMOS Setup Ut Main Advanced Peripherals PnP/PCI PC Health Bo	ility pot Exit
Reset Configuration Data [Disabled]	Item Help
Resources Controlled By [Auto(ESCD)] × IRQ Resources	Menu Level Default is Disabled. Select Enabled to reset Extended System Configuration Data ESCD) when you exit Setup if you have installed a new add-on and the system reconfiguration has caused such a serious conflict that the OS cannot boot
1↓++:Move Enter:Select +/-/PU/PD:Value F10:Save F5:Previous Values F6:Fail-Safe Defaults F	ESC:Exit F1:General Help 7:Optimized Defaults
Figure 4 PnP/PCI men	11

Resource controlled by

The Award Plug and Play BIOS has the capacity to automatically configure all of the boot and Plug and Play compatible devices. However, this capability means absolutely nothing unless you are using a Plug and Play operating system such as Windows®95. If you set this field to "manual" choose specific resources by going into each of the sub menu that follows this field (a sub menu is preceded by a ">"). The choice: Auto(ESCD), Manual.

IRQ Resources

When resources are controlled manually, assign each system interrupt a type, depending on the type of device using the interrupt.

IRQ3/4/5/7/9/10/11/12/14/15 assigned to

This item allows you to determine the IRQ assigned to the ISA bus and is not available to any PCI slot. Legacy ISA for devices compliant with the original PC AT bus specification, PCI/ISA PnP for devices compliant with the Plug and Play standard whether designed for PCI or ISA bus architecture.

The Choice: PCI Device, Reserved.

AR-B1832

4.7 Peripheral

Phoenix Main Advanced Peripherals	- AwardBIOS CMOS Setup U s PnP/PCI PC Health B	tility oot Exit
Onboard Serial Port 1		Item Help
UNDOARD Serial Port 2 UART Mode Select RxD , TxD Active IR Transmission Delay UR2 Duplex Mode Use IR Pins Onboard Serial Port 3 Onboard Serial Port 4	[Normal] [Hi,Lo] [Enabled] [Half] [IR-Rx2Tx2] [3E8/IRQ11] [2E8/IRQ10]	Menu Level 🕨
Onboard Parallel Port Parallel Port Mode EPP Mode Select ECP Mode Use DMA	[378/IRQ7] [SPP] [EPP1.7] [3]	
Onboard FDC Controller USB Controller USB 2.0 Controller AC97 Audio	[Enabled] [Enabled] [Enabled] [Auto]	
t↓↔:Move Enter:Select +/- F5:Previous Values F6	-/PU/PD:Value F10:Save 5:Fail-Safe Defaults	ESC:Exit F1:General Help F7:Optimized Defaults

Figure 5 Peripheral menu

Onboard Serial Port 1/Port 2

Select an address and corresponding interrupt for the first and second serial ports.

The choice: 3F8/IRQ4, 2E8/IRQ3, 3E8/IRQ4, 2F8/IRQ3, Disabled, Auto

UART Mode Select

Select the Function Mode for UART. The choice: IrDA, ASKIR, Normal

Onboard Serial Port 3/Port 4

Select an address and corresponding interrupt for the first and second serial ports.

The choice: 3F8/IRQ4, 2E8/IRQ3, 3E8/IRQ4, 2F8/IRQ3, Disabled, Auto

AR-B1832

Onboard Parallel Port

Select 3BC/IRQ7 to enable On Board Parallel Port as first Parallel Interface.

The choice: Disable, 378/IRQ7, 278/IRQ5, 3BC/IRQ7.

USB Controller

Select *Enabled* if your system contains a Universal Serial Bus (USB) controller and you have USB peripherals. The Choice: Enabled, Disabled.

USB 2.0 Controller

This Entry is for disable / enable EHCI controller only. The Bios itself may / may not have high speed USB support. If the Bios has high speed USB support built in, the support will be automatically turn on when high speed device were attached. The Choice: Enabled, Disabled.

AC97 AUDIO

The Choice: Auto, Disabled.

AR-B1832

4.8 PC Health

Phoenix - AwardBIOS CMOS Setup Ut	ility
Main Advanced Peripherals PnP/PCI PC Health Bo	ot Exit
CPU Temperature Sustem Temperature	Item Help
System Temperature Current CPU FAN Speed VCore(V) + 3.3 V + 5 V +12 V VBAT(V) SVSB(V) Shutdown Temperature [Disabled]	Menu Level ►
↑↓→+:Move Enter:Select +/-/PU/PD:Value F10:Save	ESC:Exit F1:General Help
F5:Previous Values F6:Fail-Safe Defaults F	7:Optimized Defaults

Figure 5 H/W Monitor menu

Shutdown Temperature

This item allows the system to reset when temperature reach the trigger level.

The Choice: Disabled, 60°C/140°F, 65°C/149°F, 70°C/158°F, 75°C/167 °F

AR-B1832

4.9 Boot

Phoenix	- AwardBIOS CMOS Setup Ut	ility
Main Advanced Peripheral	s PnP/PCI PC Health Bo	ot Exit
First Boot Device Second Boot Device Third Boot Device Boot Other Device ► Hard Disk Boot Priority	[DROK] [Hard Disk] [FloppJ [Enabled]	Item Help Menu Level ► Select Your Boot Device Priority
↑↓++:Move Enter:Select +/	-/PU/PD:Value F10:Save	ESC:Exit F1:General Help
F5:Previous Values F	6:Fail-Safe Defaults = F	7:Optimized Defaults

Figure 6 Boot menu

First/Second/Third/Other Boot Device

The BIOS attempts to load the operating system from the devices in the sequence selected in these items.

The Choice:

- ➢ Floppy.......[]
- ► LS120......[]
- ➢ Hard Disk[]
- > CDROM......[]
- > ZIP100[]
- ➢ USB-FDD[]
- ➢ USB-ZIP[]
- ➢ USB-CDROM ..[.]
- ➢ On Board LAN...[]

AR-B1832

4.10 Exit Selecting

Main	Advanced	Phoenix - Peripherals	AwardBIOS PnP/PCI	CMOS Setup PC Health	Util Boot	ity Exit E	_
Sav	e & Exit S	etup d Defaulta				Item	Help
Exi	t Without	Saving				Menu Level	•
						Save Data to	CMOS
1↓++:M F5	ove Enter	:Select +/-/	'PU/PD:Valu Fail-Safe	e F10:Save Defaults	E ES	C:Exit F1:G	eneral Help

- ➢ Save & Exit Setup
- Load Optimized Defaults
- Exit Without Saving
- Load Fail-Save Default

Figure 8 Exit menu

Save & Exit Setup

Pressing <Enter> on this item asks for confirmation:

Save to CMOS and EXIT (Y/N)? Y

Pressing "Y" stores the selections made in the menus in CMOS – a special section of memory that stays on after you turn your system off. The next time you boot your computer, the BIOS configures your system according to the Setup selections stored in CMOS. After saving the values the system is restarted again.

Load Optimized Defaults

AR-B1832

Use this menu to load the BIOS default values that are factory settings for optimal performance system operations. While Award has designed the custom BIOS to maximize performance, the factory has the right to change these defaults to meet their needs.

When you press <Enter> on this item you get a confirmation dialog box with a message similar to:

Load Optimized Defaults (Y/N) ? N

Pressing 'Y' loads the default values that are factory settings for optimal performance system operations.

Exit Without Saving

Pressing <Enter> on this item asks for confirmation:

Quit without saving (Y/N)? Y

This allows you to exit Setup without storing in CMOS any change. The previous selections remain in effect. This exits the Setup utility and restarts your computer.

Load Fail-Safe Defaults

Use this menu to load the BIOS default values that are factory settings for safety system operations.

When you press <Enter> on this item you get a confirmation dialog box with a message similar to:

Load Fail-Safe Defaults (Y/N) ? N

Pressing 'Y' loads the default values that are factory settings for Fail-Safe system operations.

AR-B1832

Appendix A. Watchdog Timer

The WDT(Watch Dog Timer) is used to generate a variety of output signals after a user programmable cont. The WDT is suitable for use in the prevention of system lock-up, such as when software becomes trapped in a deadlock. Under these sort of circumstances, the timer will count to zero and the selected outputs will be driven. Under normal circumstance, the user will restart the WDT at regular intervals before the timer counts to zero.

WATCHDOG TIMER SETTING

The watchdog timer is a circuit that maybe be used from your program software to detect crash or hang up. The watchdog timer is automatically disabled after reset. Once you enabled the watchdog timer, your program should trigger the watchdog timer every time before it times out. After you trigger the watchdogtimer, the timer will be set to zero and start to count again. If your program fails to trigger the watchdog timer before times out, it will generate a reset pulse to reset the system or trigger the IRQ9 signal in order to tell your system that the watchdog time is out.

User could test watch dog function under 'Debug.exe' program as follows:

WDT Example	Description
o 2e 87	;Extended Functions Enable Register
o 2e 87	;Extended Functions Enable Register
o 2e 07	;EFIR=EFER(Extended Function Index
	Register)point to Logical Device Number Reg.
o 2f 08	;EFDR=EFIR+1, select logical device 8
o 2e 30	;select CR30
o 2f 01	;update CR30 with value 01H
o 2e f6	;select CRF6
o 2f 08	;update CRF6 with value 08H,(8sec reset)
g	;go

AR-B1832

Appendix B: Digital I/O

One characteristic of digital circuit is its fast response to high or low signal. This kind of response is highly needed for harsh and critical industrial operating environment. Digital Input and Output, generally, are control signals. You can use these signals to control external devices that needs On/Off circuit or TTL devices. You can read or write data to the selected address to enable the function of digital IO.

Users could	test	GPIO	function	under	'Debug.	exe'	program	as
follow:								

Tellem	
GPO Example	Description
O 2E 87	; Eetended Functions Enable Register
O 2E 87	; Eetended Functions Enable Register
O 2E 2A	;Select CR2A
O 2F FC	; (Define the PINs as GPIO or Game Port 1)
	"FC" Pin 121~128 set as GPIO
O 2E 07	;EFIR=EFER(Extended Functions Index
	Register)point to Logical Device Number Reg.
O 2F 07	;EFDR=EFIR+1(select logical device 7,GPIO in
	logical device 7)
O 2E 30	;select CR30(Active or inactive)
O 2F 01	;set 01(Active), 00(inactive)
O 2E F0	;select CRF0(Set the PINs be GPO or GPI
	Function)
O 2F 00	; set the PINs be GPO
O 2E F2	;select CRF2(Output High/Low)
O 2F 00	;set the PINs be all Low Level(FF=all High
	Level)
O 2E F1	;select CRF1
O 2F FF	;set the Output to be FF(all High)
O 2E AA	;exit EFER
Q	;quit debug

AR-B1832

GPI Example	Description
O 2E 87	; Extended Functions Enable Register
O 2E 87	; Extended Functions Enable Register
O 2E 2A	;select CR2A
O 2F FC	; (Define the PINs as GPIO or Game
	Port1)"FC"Pin121~128 set as GPIO
O 2E 07	;EFIR=EFER(Extended Functions Index
	Register)point to Logical Device Number Reg.
O 2F 07	;EFDR=EFIR+1(select logical device 7,GPIO in
	logical device7)
O 2E 30	;select CR30(Active or inactive)
O 2F 01	;set 01(Active), 00(Inactive)
O 2E F0	;select CRF0(Set the PINs be GPO or GPI
	Funciton)
O 2F FF	; set the PINs be GPI
O 2E F1	;select CRF1(Set the PINs be Read only)
I 2F	;Show the PINs Value
Q	;Quit debug

AR-B1832

Appendix C: I/O Address Map

I/O ADDRESS MAP

I/O AE [,] DRESS MAP	DESCRIPTION
00000000-0000000F	DMA Controller
00000000-00000CF7	PCI Bus
00000010-0000001F	Mainboard Resource
00000020-00000021	Programable Interrupt Controller
00000022-0000003F	Mainboard Resource
00000040-00000043	System Timer
00000044-0000005F	Mainboard Resource
00000060-00000060	Standard 101/102 keyboard Controller
00000061-00000061	System Speaker
00000062-00000063	Mainboard Resource
00000064-00000064	Standard 101/102 keyboard Controller
00000065-0000006F	Mainboard Resource
00000070-00000073	System CMOS/RTC
00000074-0000007F	Mainboard Resource
0000080-0000090	DMA Controller
00000091-00000093	Mainboard Resource
00000094-0000009F	DMA Controller
000000A0-000000A1	Programable interrupt controller
000000A2-000000BF	Mainboard Resource
000000C0-000000DF	DMA Controller
000000E0-000000EF	Mainboard Resource
000000F0-000000FF	Math Coprocessor
00000170-00000177	Secondary IDE Channel
000001F0-000001F7	Primary IDE Channel
00000274-00000277	ISAPNP Read Data Port
00000279-00000279	ISAPNP Read Data Port
00000294-00000297	Mainboard Resource
000002E8-000002EF	COM4
000002F8-000002FF	COM2
00000376-00000376	Secondray IDE Channel
00000378-0000037F	LPT

AR-B1832

Graphics Controller
Graphics Controller
COM3
Primary IDE Channel
COM1
Mainboard Resource
Mainboard Resource
SMBus
LPT
Mainboard Resource
Mainboard Resource
Mainboard Resource
PCI Bus
Mainboard Resource
Mainboard Resource
Mainboard Resource
100M Network Connection
100M Network Connection #2
AC'97 Audio
USB Universal Host Controller
Graphics Controller
USB Universal Host Controller
AC'97 Audio
USB Universal Host Controller
Ultra ATA Storage Controller

1ST MB MEMORY ADDRESS MAP

MEMORY ADDRESS	DESCRIPTION
00000000-0009FFFF	System Mainboard
000A0000-000BFFFF	Graphics Controller
000A0000-000BFFFF	PCI Bus
000C0000-000DFFFF	PCI Bus
000CCA00-000CFFFF	System Mainboard
000E0000-000EFFFF	System Mainboard
000F0000-000F7FFF	System Mainboard
000F8000-000FBFFF	System Mainboard

AR-B1832

000FC000-000FFFFF System Mainboard 00100000-1DFEFFFF System Mainboard 1DFF0000-1DFFFFFF System Mainboard 1E000000-FEBFFFFF PCI Bus D8000000-DFFFFFFF Graphics Controller E0000000-E7FFFFFF Graphics Controller E8000000-E801EFEF 100M Network connection
00100000-1DFEFFFF System Mainboard 1DFF0000-1DFFFFFF System Mainboard 1E000000-FEBFFFFF PCI Bus D8000000-DFFFFFFF Graphics Controller E0000000-E7FFFFFF Graphics Controller E8000000-E801EEEE 100M Network connection
1DFF0000-1DFFFFFFSystem Mainboard1E000000-FEBFFFFFPCI BusD8000000-DFFFFFFFGraphics ControllerE0000000-E7FFFFFGraphics ControllerE8000000-E801EEE100M Network connection
1E000000-FEBFFFFF PCI Bus D8000000-DFFFFFF Graphics Controller E0000000-E7FFFFFF Graphics Controller E8000000-E801EEE 100M Network connection
D8000000-DFFFFFFF Graphics Controller E0000000-E7FFFFF Graphics Controller E8000000-E801EEEE 100M Network connection
E000000-E7FFFFF Graphics Controller
E800000-E801EEEE 100M Network connection
E8020000-E803FFFF 100M Network connection #2
E8050000-E8050FFF 100M Network connection
E8051000-E8051FFF 100M Network connection #2
E8100000-E817FFFF Graphics Controller
E8180000-E81FFFFF Graphics Controller
E8200000-E82003FF USB2 Enhanced Host Controller
E8201000-E82011FF AC'97 Audio
E8202000-E82020FF AC'97 Audio
FEBFFC00-FEBFFFFF Ultra ATA Storage Controller
FEC00000-FECFFFFF System Mainboard
FEE00000-FEEFFFFF System Mainboard
FFB00000-FFB7FFFF System Mainboard
FFB00000-FFBFFFFF Firmware Hub Device
FFF80000-FFFFFFFF System Mainboard

AR-B1832

IRQ Mapping Chart

IRQ0(ISA)	System Timer	IRQ15(ISA)	Secondary IDE
IRQ1(ISA)	Keyboard	IRQ9(PCI)	SMBus
IRQ3(ISA)	COM2	IRQ16(PCI)	USB Universal
IRQ4(ISA)	COM1	IRQ16(PCI)	Graphics
IRQ8(ISA)	System CMOS/RTC	IRQ17(PCI)	AC'97
IRQ9I(ISA)	ACPI-Compliant	IRQ18(PCI)	USB Universal
IRQ10(ISA)	COM4	IRQ19(PCI)	USB Universal
IRQ11(ISA)	COM3	IRQ20(PCI)	Network #2
IRQ12(ISA)	Mouse	IRQ21(PCI)	Network
IRQ13(ISA)	Math Coprocessor	IRQ23(PCI)	USB2 Enhanced
IRQ14(ISA)	Primary IDE		

DMA Channel Assignments

CHANNEL	FUNCTION
0	Available
1	Available
2	Available
3	Available
4	Cascade for DMA controller 1
5	Available
6	Available
7	Available

AR-B1832